



10002505-2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Sharma

Serial No.: 10/615,532

Filing Date: July 7, 2003

For: MULTIPLE ECC SCHEMES
TO IMPROVE BANDWIDTH

Examiner: Baker, Stephen

Art Unit: 2133

Hon. Commissioner of Patents and Trademarks
Alexandria, VA 22313-1450

TERMINAL DISCLAIMER TO OBVIATE
DOUBLE PATENTING REJECTION (37 CFR § 1.321(c))

Dear Sir:

1. Name of Assignee: Hewlett Packard Co.
Address of Assignee: 11000 Wolfe Road
Cupertino, California 95014-0691

2. The Assignee certifies to the best of Assignee's knowledge and belief that Assignee has the entire right, title, and whole interest in and to the above referenced patent application, and represents that the undersigned is authorized to sign on behalf of the above-referenced Assignee.

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Terminal Disclaimer
Examiner: Baker, Stephen

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Serial No. 10/615,532
Group Art Unit: 2133